

FIG. 1  
 (Prior Art)

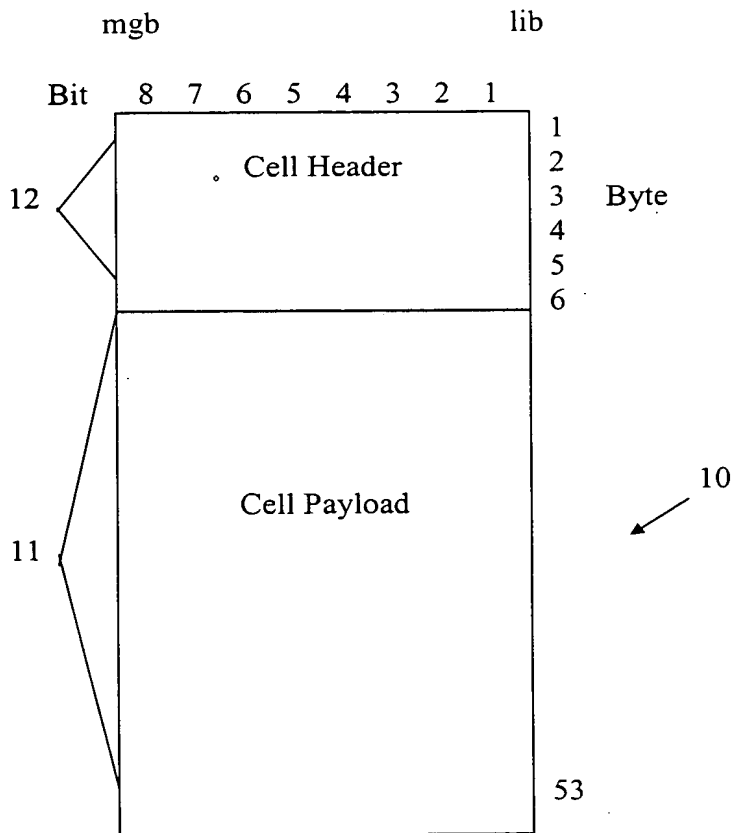


Fig. 1a

Fig. 1b

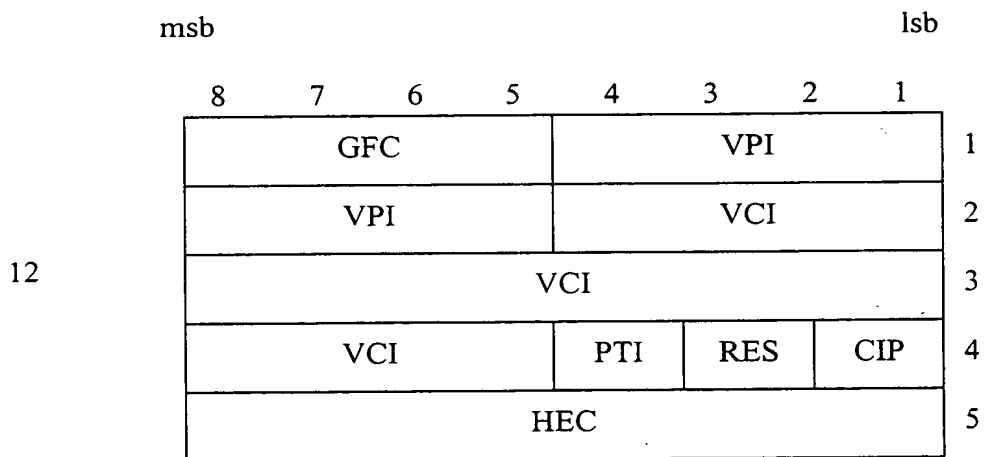
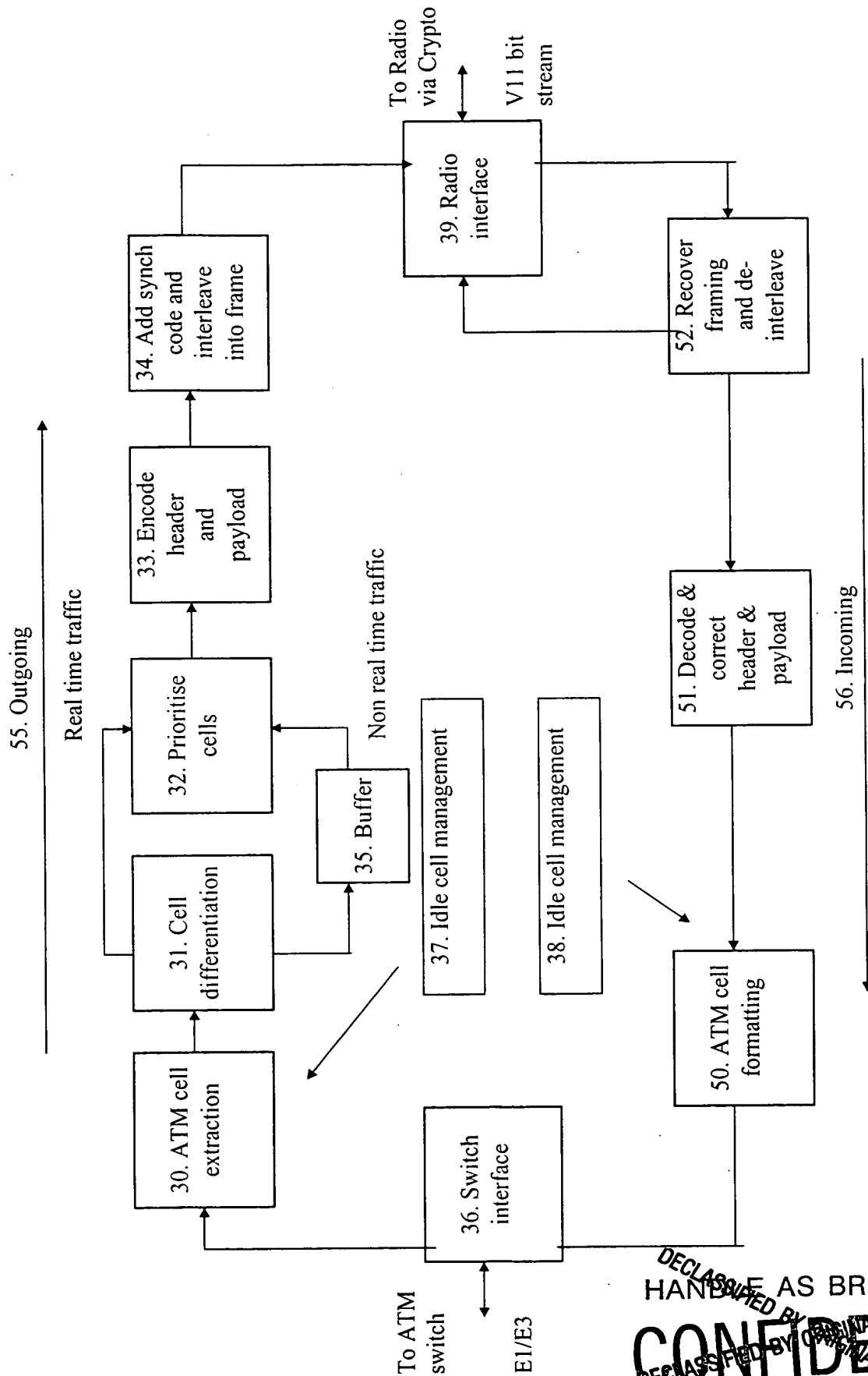


Diagram illustrating the frame structure and bit allocation:

- Encoded payload:**  $62 \times 8 = 496$  bits (labeled 20).
- Encoded header:**  $(15 \times 4) + 4 = 64$  bits (labeled 21).
- Sync word:** 31 bits (labeled 22).

The frame is composed of these three parts, with "etc" indicating repetition. The frame is divided into segments, with a detailed view of the frame end showing the 558<sup>th</sup> bit (black runs out), the 570<sup>th</sup> bit (grey runs out), and the 591<sup>st</sup> bit (all bits used).

Fig. 3



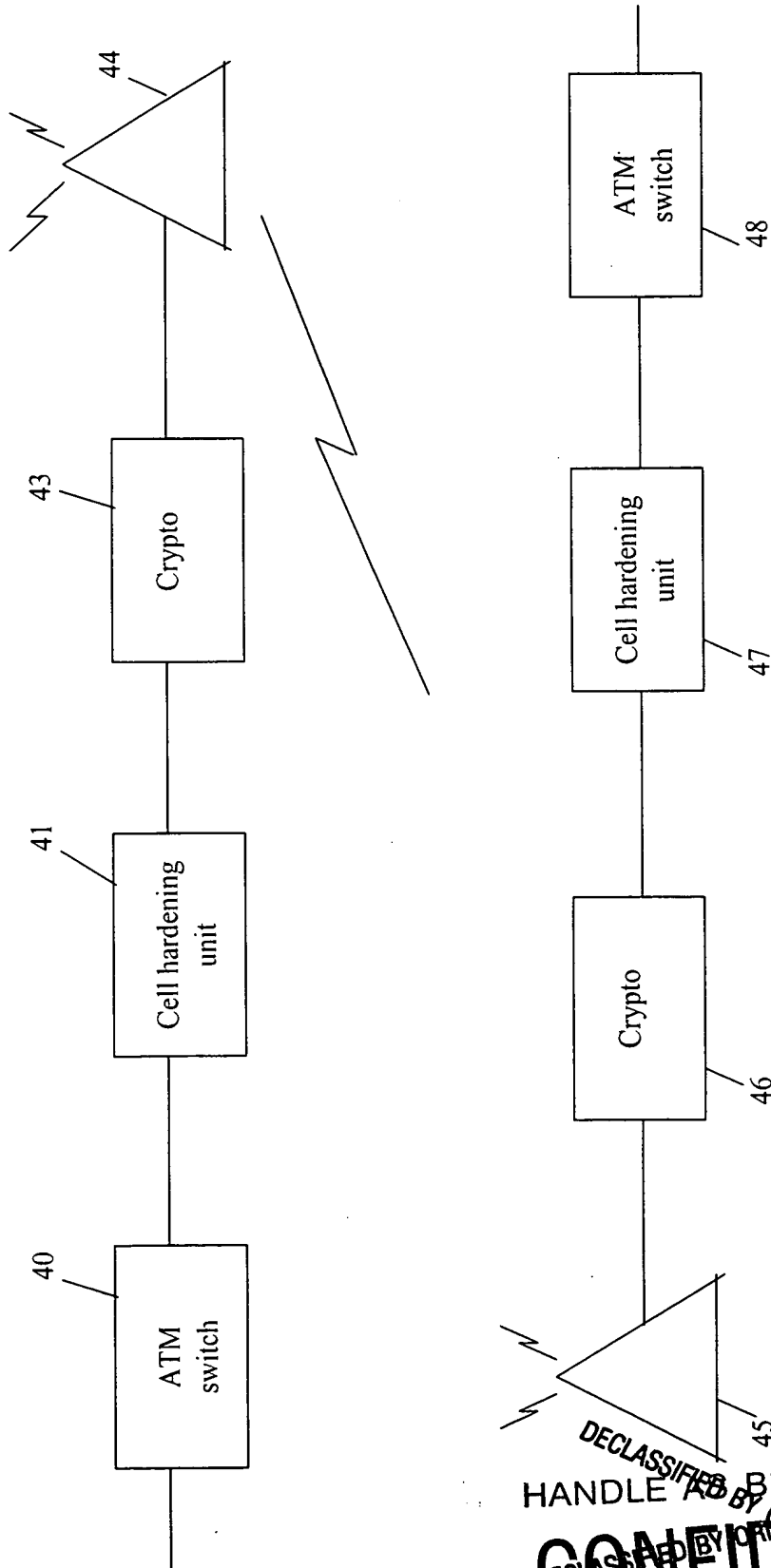


Fig. 4

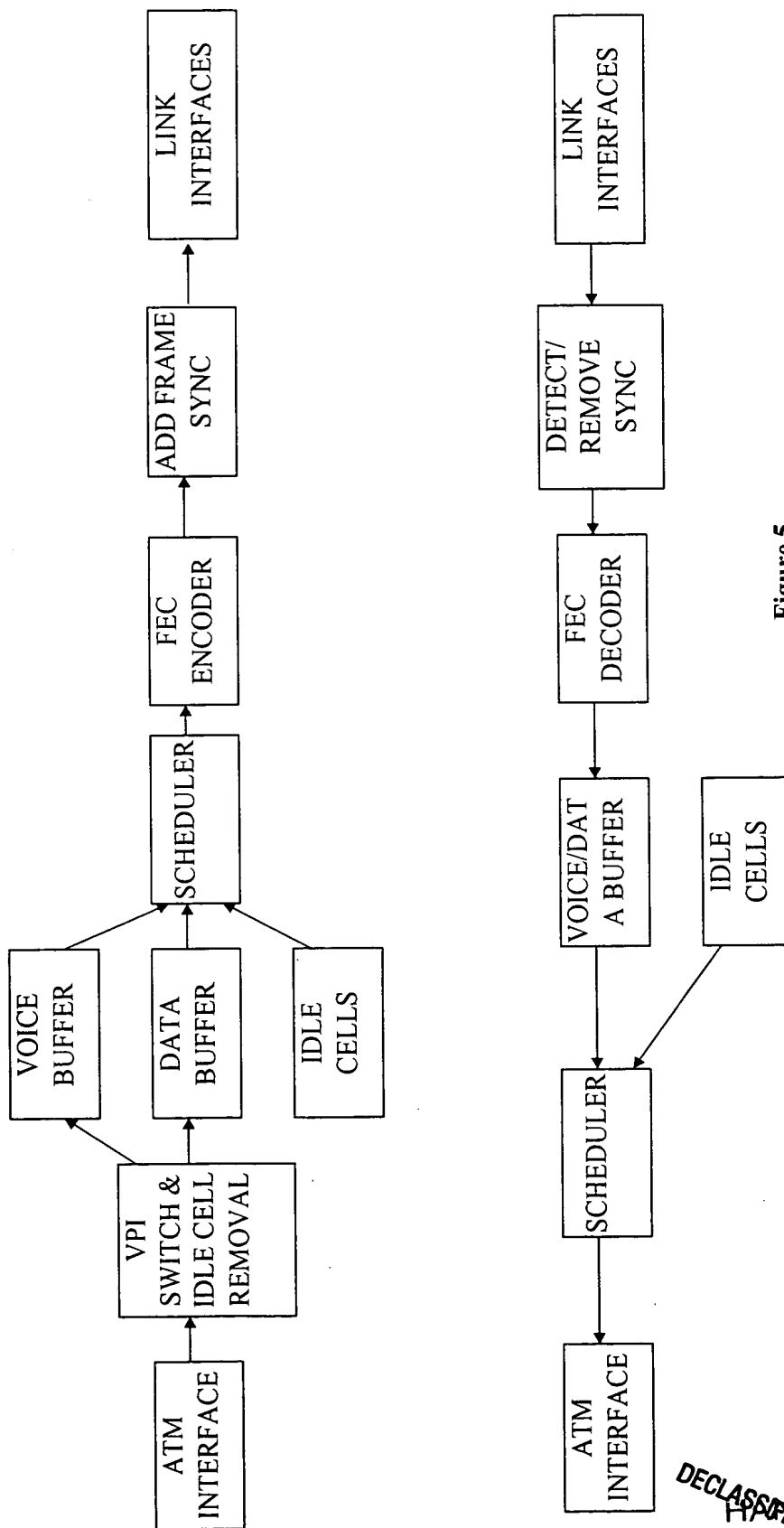
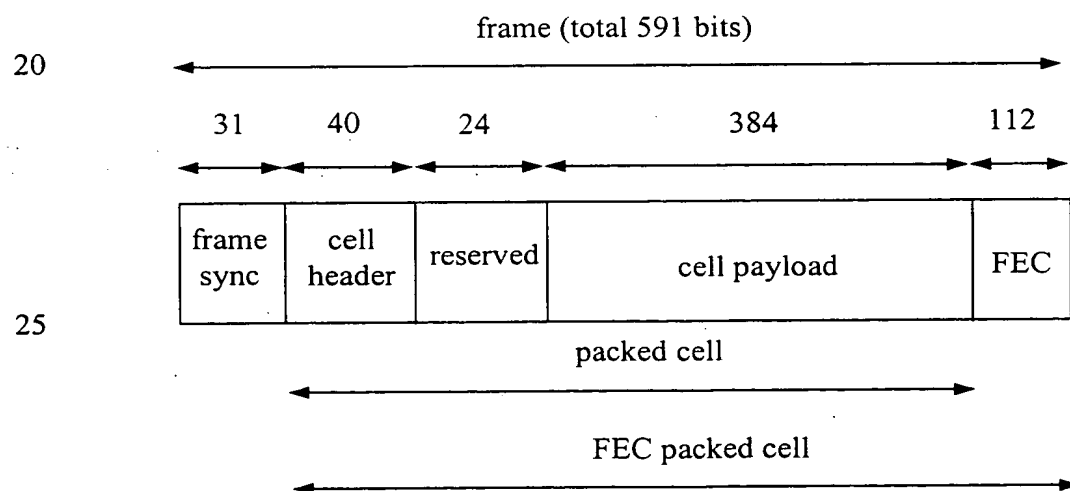
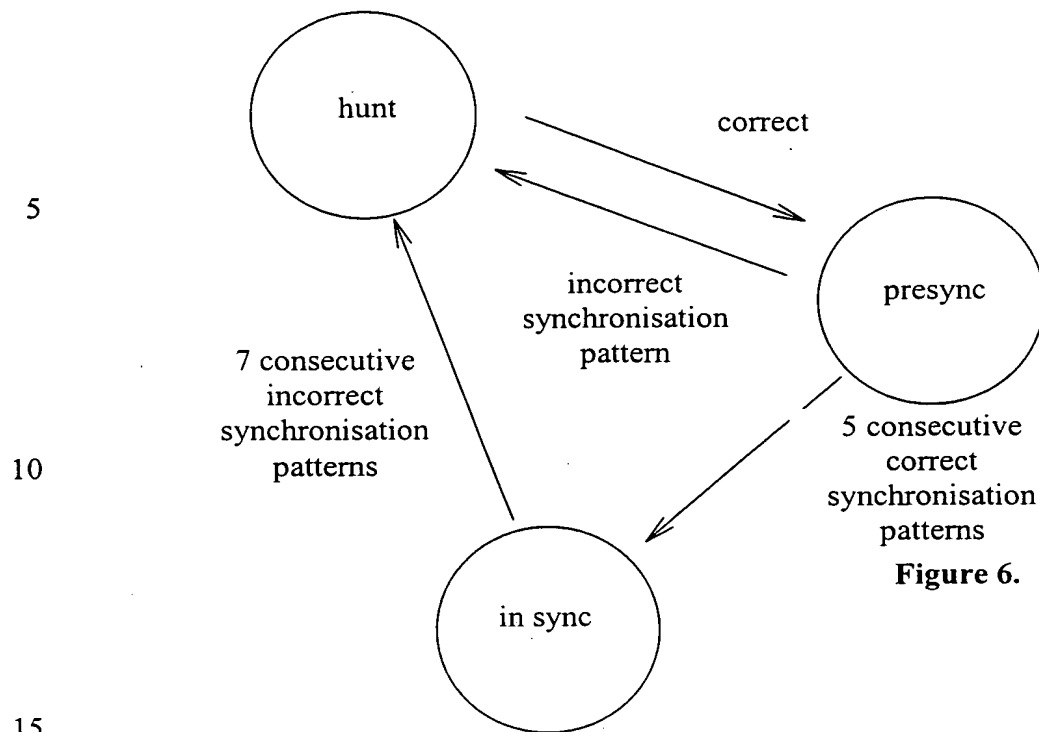


Figure 5.

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**Figure 7.**